DETAILED ACTION

1.   As per the instant Application having Application number 10/754,483, the

examiner acknowledges the applicant's submission of the amendment amendments filed

2/7/2011.

INFORMATION DISCLOSURE STATEMENT

2.   As required by **M.P.E.P. 609(C)**, the applicant's submissions of the Information

Disclosure Statements dated 2/8/2011 and 2/11/2011 are acknowledged by the examiner

and the cited references have been considered in the examination of the claims now

pending. As required by **M.P.E.P 609 C(2)**, a copy of the PTOL-1449 initialed and dated

by the examiner is attached to the instant office action.

EXAMINER'S AMENDMENT

3.   An examiner's amendment to the record appears below. Should the changes

and/or additions be unacceptable to applicant, an amendment may be filed as provided by

37 CFR 1.312. To ensure consideration of such an amendment, it MUST be submitted no

later than the payment of the issue fee.

4.   Authorization for this examiner's amendment was given in a telephone interview

with David P. Olynick (Reg. No. 48,615) on 3/31/2011.

5.   The claims have been amended as follows:

Claim 72 (Currently Amended)

A method for operating a memory card that includes 1) a host controller configured to

communicate with a host device, 2) at least an initial volume and 3) a switch wherein the

memory card provides non-volatile data storage having an address space defined by a

contiguous range of addresses, the method in the host controller comprising:

(a) retrieving volume information from the initial volume stored in a range of

addresses that is a part of the contiguous range of addresses that defines the address

space;

(b) determining, based on the volume information, whether the initial volume uses

a 16- bit addressing, **uses greater than the 16-bit addressing** or uses less than the 16-bit

addressing;

(c) when said determining (b) determines the initial volume uses greater than the

16-bit addressing, by-passing a switch position of the switch, determining the memory

card uses 32-bit addressing and communicating to the host via the host controller to use

the memory card as a single volume using 32-bit addressing and; and

(d) when said determining (b) determines that the initial volume uses the 16-bit

addressing or uses less than the 16-bit addressing,

(1) reading the switch position of the switch on the memory card wherein

the switch position of the switch is only used when the host controller determines

16-bit addressing or less is used;

(2) determining an address offset for the address space based on upon the

switch position wherein the address offset corresponds to one of a plurality of

volumes in which the memory card is partitioned;

(3) communicating to the host device via the host controller to use the

volume of the memory card indicated by the switch position and the address

offset.

REASONS FOR ALLOWANCE

6.   The following is an examiner's statement of reasons for allowance:

7.   Per the instant office action, claims **72-80** are considered as allowable subject

matter.

The reasons for allowance of independent claim 72 are that the prior art of record,

neither anticipates, nor renders obvious the recited combination as a whole. The prior art

of record neither anticipates, nor renders obvious the recited combination of "…

retrieving volume information from the initial volume stored in a range of addresses that

is a part of the contiguous range of addresses that defines the address space… when said

determining (b) determines the initial volume uses greater than the 16-bit addressing, by-

passing a switch position of the switch, determining the memory card uses 32-bit

addressing and communicating to the host via the host controller to use the memory card

as a single volume using 32-bit addressing and; and (d) when said determining (b)

determines that the initial volume uses the 16-bit addressing or uses less than the 16-bit

addressing, (1) reading the switch position of the switch on the memory card wherein the

switch position of the switch is only used when the host controller determines 16-bit

addressing or less is used… (3) communicating to the host device via the host controller

to use the volume of the memory card indicated by the switch position and the address

offset.", in combination with all the other limitations in independent claim 72.

Dependent claims 73-80 are allowable at least for the reasons recited above as including all of the limitations of the allowable independent base claims upon which they depend.

8.   Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

DIRECTION OF FUTURE CORRESPONDENCES

9.   Any inquiry concerning this communication or earlier communications from the examiner should be directed to Yaima Campos whose telephone number is (571)272-1232 and email address is yaima.campos@uspto.gov. The examiner can normally be reached on Monday to Friday 8:30 AM to 5:00 PM.

10.   If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Sanjiv Shah can be reached on (571) 272-4098. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

11.   Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

March 31, 2011   /Yaima Campos/
                                                                 Examiner, Art Unit 2185